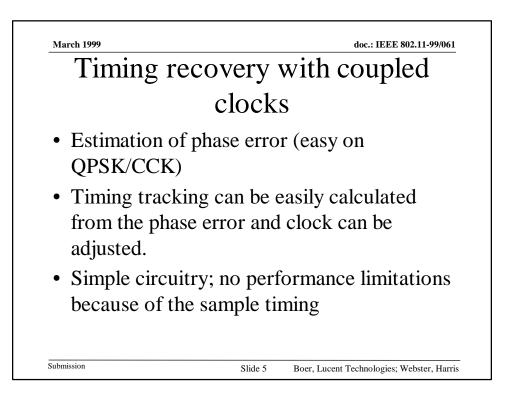


Timin	g recovery #1
• Analyze zero cr CCK signal.	ossings at the incomming
 Zero crossings environment 	very noisy, especially in a real
 Timing tracking ns delayspread 	g becomes unreliable at about 50
• relatively simple	e circuit, low performance

March 1999	doc.: IEEE 802.	11-99/061
Tir	ning recovery #2	
• Early late d Filter	etection after Channel Match	ed
	o run at twice the chiprate, extra calculate the interchip values	
e	r CMF/ Walshcorrelation still ver et of channel estimation on ce	ſy
– Timing tra delaysprea	cking reliable up to 200-250 ns d.	
ubmission	Slide 4 Boer, Lucent Technologies; Web	ster, Harri



March 1999		doc.: IEEE 802.11-99/061
	Propos	sal
• Normally (ust because	of the bill of
materials) c	locks are co	upled. In some
application	s the clocks h	nave to be
uncoupled.		
• Notify the	eceiver thro	ugh the service field
		led, such that
	-	f this for optimal
performanc		· · · · · ·
ronnune	. .	
Submission	Slide 6	Boer, Lucent Technologies; Webster, Harris